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PATENT ABSTRACTS OF JAPAN

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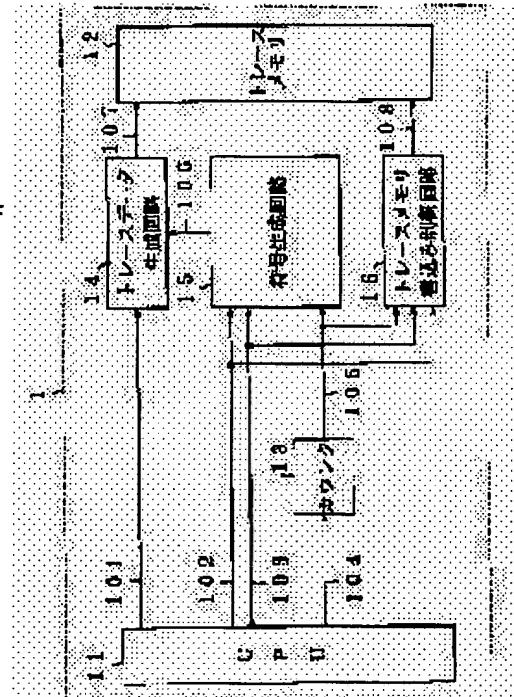
NISHIMURA TATSUYA

(54) IN-CIRCUIT EMULATOR

(57)Abstract:

PROBLEM TO BE SOLVED: To compress the capacity of a trace memory.

SOLUTION: When a condition branching instruction is executed, a code corresponding to the executed result of that condition branching instruction is written in a trace memory 12. Besides, the clock of a CPU is counted and for each fixed step signal, a code showing the fixed step signal and an execution address are written in the trace memory 12. In the case of interruption/indirect branching instruction, a code showing that instruction and branch destination and branch source addresses are written in the trace memory 12. Thus, the amount of trace data in respect to the execution of a frequent non-condition branching instruction or condition branching instruction is decreased and the capacity of the trace memory 12 can be remarkably reduced.



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